



**CSMSS**  
Chhatrapati Shahu Maharaj Shikshan Sanstha's  
**CHH. SHAHU COLLEGE OF ENGINEERING**  
Kenchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 011



ELECTRONICS ENGG. (VLSI D & T)					CLASS TIME TABLE					FORM NO.	AC-07A
										REV. NO.	ISSUE DATE
ACADEMIC YEAR:					SEMESTER:					CLASS	Second Year Third year
2025-26					IV					CLASS ROOM NO.	A-208 A-209
W.E.F.					2-Mar-2026						
Time	Class	10:00 to 11:15	11:15 to 12:15	12:15 to 01:00	01:00 to 02:00	02:00 to 03:00	03:00 to 03:15	03:15 to 04:15	04:15 to 05:15	CLASS ROOM NO.	
Day		1	2	3	4	5	6				
MON	SY	MDM GRB	SDV JRS	AC LAB (BATCH SA) A-201 TAM MDM LAB (BATCH SB) A-203 GRB SYS. VERILOG LAB (BATCH SC) A-206 KBD				NTSS RMC	HSSM APS	A-208	
	TY	MP (BATCH TAB) A -306 TAM MP (BATCH TAC) A 201 PNK VAC (BATCH TAA) A-203 KBD		IoT GGP	DE APS	IOT LAB (BATCH TAB) A-306 GGP DVD LAB (BATCH TAC) A 206 JRS OEC-2 LAB (BATCH TAA) A-203 PNK				A-209	
TUE	SY	AC SJH	NTSS RMC	AC LAB (BATCH SC) A-201 TAM MDM LAB (BATCH SA) A-203 GRB SYS. VERILOG LAB (BATCH SB) A-206 KBD				MDM GRB	CoI HGA	A-208	
	TY	IoT GGP	DVD JRS	DE APS	CN RMC TUT SMSC HGA		IOT LAB (BATCH TAC) A-306 GGP DVD LAB (BATCH TAA) A 206 JRS OEC-2 LAB (BATCH TAB) A-203 KBD			A-209	
WED	SY	NTSS RMC	OE APS	AC LAB (BATCH SB) A-201 TAM MDM LAB (BATCH SC) A-203 GRB SYS. VERILOG LAB (BATCH SA) A-206 KBD				AC TAM	LBDBA HGA	A-208	
	TY	CC PNK AI&ML KBD	IoT GGP	CN SJH SMSC HGA	DE APS	IOT LAB (BATCH TAA) A-306 GGP DVD LAB (BATCH TAB) A 206 JRS OEC-2 LAB (BATCH TAC) A-203 KBD				A-209	
THU	SY	PCB LAB (BATCH SA) A-206 GGP NTSS LAB (BATCH SB) A-201 HGA PCB LAB (BATCH SC) A-203 GRB		P&IPR PNK	AC TAM	OE APS			MDM GRB	A-208	
	TY	DVD JRS	CC PNK AI&ML KBD	SOFT SKILL MR RAMAN KARDE			IoT TUT GGP			CN RMC SMSC HGA	A-209
FRI	SY	AC TAM	NTSS RMC	SDV JRS	OE APS	PCB LAB (BATCH SC) A-203 GRB NTSS LAB (BATCH SA) A-201 HGA PCB LAB (BATCH SB) A-206 APS				A-208	
	TY	APTITUDE CLASS Mr. Ajay Butwani		MP (BATCH TAC) A -306 TAM MP (BATCH TAA) A 201 PNK VAC (BATCH TAB) A-203 RMC			DVD JRS			CC PNK AI&ML KBD	A-209
SAT	SY	HSSM APS	CoI HGA	PCB LAB (BATCH SB) A-206 RMC NTSS LAB (BATCH SC) A-201 HGA PCB LAB (BATCH SA) A-203 GGP			SDV JRS			P&IPR PNK	
	TY	MP (BATCH TAA) A -306 TAM MP (BATCH TAA) A 201 PNK VAC (BATCH TAC) A-203 RMC		DVD JRS	CC PNK AI&ML KBD	DE APS			CN RMC SMSC HGA		

*(Signature)*

Mr. G. R. Bhalekar  
Time Table Coordinator

*(Signature)*

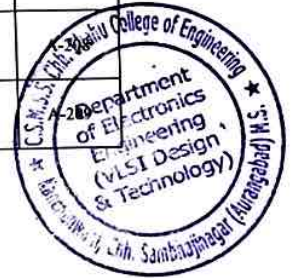
Dr. S. J. Honade  
HOD

*(Signature)*

Dr. K. N. Pathan  
Dean Academics

*(Signature)*  
4/13/26

Dr. G. B. DONGRE  
PRINCIPAL





FORM NO.	AC-07A
REV. NO. ISSUE DATE	00, December 2024
CLASS	M.TECH 1ST YEAR
CLASS ROOM NO.	A-301
W.E.F.	29/12/2025

<b>ELECTRONICS ENGG. (VLSI D &amp; T)</b>		<b>CLASS TIME TABLE</b>	
ACADEMIC YEAR:	2025-26	SEMESTER:	II

Time Day	10:00 to 11:15	11:15 to 12:15	12:15 to 01:00	01:00 to 02:00	02:00 to 03:00	03:00 to 03:15	03:15 to 04:15	04:15 to 05:15
	1	2		3	4		5	6
MON	ADVD SJH	RTES - TUT GGP		VDVT JRS	AI^ML RMC		IKS TAM	
TUE	AI^ML RMC	VDVT HGA		ADVD-TUT SJH	RTES GGP		IPR PNK	DM APS
WED	VDVT-TUT JRS	RTES GGP		IPR PNK				
THU	AI^ML RMC	IKS TAM		ADVD KBD	RTES GGP			
FRI	VDVT HGA	IPR PNK		PG Lab – III (A-206) HGA				
SAT	ADVD KBD	DM APS		PG Lab – IV (A 302) APS				

Subject Name	Faculty Name
Analog & Digital VLSI Desien	DR. S. J. Honade/ Mrs. K. B. Dandge
VLSI Design Verification & Testing	DR. J. R. SHINDE/ MR H G AMBHORE
Real Time Embedded Systems	MR. G. G. PATIL/ Mr. G. R. BHALEKAR
Artificial Intelligence & Machine learning	Mr. R.M.Chudiwla
Intellectual Property Rights	Mr P. N Kathar
IKS Bucket	Mr T. A. Mohije
Disaster Management	Mr. A. P Satpute

Practical/ Lab	Faculty Name
PG Lab – III	Mr H.G Ambhore
PG Lab – IV	Mr. A. P Satpute

Lab Name	Room Number
Smart Classroom	A 206
Commnication Lab	A-302

CLASS TEACHER	DR. J. R. SHINDE
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Mr. G. R. Bhalekar  
Time Table Coordinator

Dr. S. J. Honade  
HOD

Dr. K. N. Pathan  
Dean Academics

Dr. G. B. DONGRE  
PRINCIPAL



FORM NO.	AC-07A
REV. NO. ISSUE DATE	00, December 2024
CLASS	Third Year
CLASS ROOM NO.	A-209
W.E.F.	2-Mar-2026

<b>ELECTRONICS ENGG. (VLSI D &amp; T)</b>		<b>CLASS TIME TABLE</b>	
ACADEMIC YEAR:	2025-26	SEMESTER:	VI

Time	10:00 to 11:15	11:15 to 12:15	12:15 to 01:00	01:00 to 02:00	02:00 to 03:00	03:00 to 03:15	03:15 to 04:15	04:15 to 05:15
Day	1	2		3	4		5	6
MON	MP (BATCH TAB) A -306 TAM MP (BATCH TAC) A 201 PNK VAC (BATCH TAA) A-203 KBD			IoT GGP	DE APS		IOT LAB (BATCH TAB) A-306 GGP DVD LAB (BATCH TAC) A 206 JRS OEC-2 LAB (BATCH TAA) A-203 PNK	
TUE	IoT GGP	DVD JRS		DE APS	CN RMC TUT SMSC HGA		IOT LAB (BATCH TAC) A-306 GGP DVD LAB (BATCH TAA) A 206 JRS OEC-2 LAB (BATCH TAB) A-203 KBD	
WED	CC PNK AI&ML KBD	IoT GGP		CN SJH SMSC HGA	DE APS		IOT LAB (BATCH TAA) A-306 GGP DVD LAB (BATCH TAB) A 206 JRS OEC-2 LAB (BATCH TAC) A-203 KBD	
THU	DVD JRS	CC PNK AI&ML KBD		SOFT SKILL MR RAMAN KARDE			IoT TUT GGP	CN RMC SMSC HGA
FRI	APTITUDE CLASS Mr. Ajay Butwani			MP (BATCH TAC) A -306 TAM MP (BATCH TAA) A 201 PNK VAC (BATCH TAB) A-203 RMC			DVD JRS	CC PNK AI&ML KBD
SAT	MP (BATCH TAA) A -306 TAM MP (BATCH TAAB) A 201 PNK VAC (BATCH TAC) A-203 RMC			DVD JRS	CC PNK AI&ML KBD		DE APS	CN RMC SMSC HGA

Subject Name	Faculty Name
Digital VLSI Design	DR. J. R. SHINDE
Internet of Things & Industry 4.0	MR. G. G. PATIL
Computer Networks	Mr. R. M. Chudiwal
Artificial Intelligence & Machine Learning	MRS. K. B. DANDGE
Cloud Computing	MR. P. N. KATHAR
Development Engineering	MR. A. P. SATPUTE
Semiconductor Materials Synthesis and Characterization	MR H G AMBHORE

Practical/ Lab	Faculty Name
Digital VLSI Design Lab	DR. J. R. SHINDE
Internet of Things & Industry 4.0 Lab	MR. G. G. PATIL
OEC-2 Lab (CC LAB)	MR. P. N. KATHAR
OEC-2 Lab (AI&ML LAB)	MRS. K. B. DANDGE
Mini Project II	MR T A MOHIJE
VAC	RMC & KBD
APTITUDE CLASS	MR. AJAY BUTWANI
SOFT SKILL	MR RAMAN KARDE

Lab Name	Room Number
Programming Lab	A 203
Smart Classroom	A 206
EDC Lab	A-201
IOT & ES LAB	A-306

CLASS TEACHER	Mr. T. A. Mohije
TG: TA	MR. R.M. CHUDI WAL
TG: TB	MR. A. P. SATPUTE
TG: TC	MR. P. N. KATHAR

BATCHES
BATCH: TAA
BATCH: TAB
BATCH:- TAC

Mr. G. K. Bhalekar  
Time Table Coordinator

Dr. S. J. Honade  
HOD

Dr. K. N. Pathan  
Dean Academics

Dr. G. B. DONGRE  
PRINCIPAL





**CSMSS**  
Chhatrapati Shahu Maharaj Shiksha Sanstha  
**CHH. SHAHU COLLEGE OF ENGINEERING**  
Kanchanwadi, Patthen Road, Chhatrapati Sambhajinagar 431 011



FORM NO.	AC-07A
REV. NO. ISSUE DATE	00, December 2024
CLASS	Second Year
CLASS ROOM NO.	A-208
W.E.F.	2-Mar-2026

<b>ELECTRONICS ENGG. (VLSI D &amp; T)</b>		<b>CLASS TIME TABLE</b>	
ACADEMIC YEAR:	2025-26	SEMESTER:	IV

Time	10:00 to 11:15	11:15 to 12:15	12:15 to 01:00	01:00 to 02:00	02:00 to 03:00	03:00 to 03:15	03:15 to 04:15	04:15 to 05:15
Day	1	2		3	4		5	6
MON	MDM GRB	SDV JRS		AC LAB (BATCH SA) A-201 TAM MDM LAB (BATCH SB) A-203 GRB SYS. VERILOG LAB (BATCH SC) A-206 KBD			NTSS RMC	HSSM APS
TUE	AC SJH	NTSS RMC		AC LAB (BATCH SC) A-201 TAM MDM LAB (BATCH SA) A-203 GRB SYS. VERILOG LAB (BATCH SB) A-206 KBD			MDM GRB	CoI HGA
WED	NTSS RMC	OE APS		AC LAB (BATCH SB) A-201 TAM MDM LAB (BATCH SC) A-203 GRB SYS. VERILOG LAB (BATCH SA) A-206 KBD			AC TAM	LBDBA HGA
THU	PCB LAB (BATCH SA) A-206 GGP NTSS LAB (BATCH SB) A-201 HGA PCB LAB (BATCH SC) A-203 GRB			P&IPR PNK	AC TAM		OE APS	MDM GRB
FRI	AC TAM	NTSS RMC		SDV JRS	OE APS		PCB LAB (BATCH SC) A-203 GRB NTSS LAB (BATCH SA) A-201 HGA PCB LAB (BATCH SB) A-206 APS	
SAT	HSSM APS	CoI HGA		PCB LAB (BATCH SB) A-206 RMC NTSS LAB (BATCH SC) A-201 HGA PCB LAB (BATCH SA) A-203 GGP			SDV JRS	P&IPR PNK

Subject Name	Faculty Name
Network Theory & Signals and Systems	Mr. R. M. Chudiwal
Analog Circuits	MR. T. A. MOHIJE
System Design using Verilog	DR. J. R. SHINDE
Life of Bharatratna Dr. Babasaheb Ambedkar	Mr. H. G. Ambhore
Constitution of India	Mr. H. G. Ambhore
MDM	MR. G. R. BHALEKAR
Patents and IPR	MR. P. N. KATHAR
Open Elective	MR. A. P. SATPUTE
HSSM (Marathi)	MR. A. P. SATPUTE

Practical/ Lab	Faculty Name
Analog Circuits Lab	MR. T. A. MOHIJE
Network Theory & Signals and Systems Lab	Mr. H. G. Ambhore
MDM Lab	MR. G. R. BHALEKAR
System Design using Verilog Lab	MRS. K. B. DANDGE
PCB Designing	APS+RMC,GGP,GRB

Lab Name	Room Number
Programming Lab	A 203
Smart Classroom	A 206
EDC Lab	A-201

CLASS TEACHER	Mr. T. A. Mohije
TG: SA	MR. G. G. PATIL
TG: SB	Dr. J. R. Shinde
TG: SC	Mrs. K. B. Dandge

BATCHES	
BATCH: SA	VL 2101-2123
BATCH: SB	VL 2124-2146
BATCH: SC	VL 2147-2172

Mr. G. R. Bhalakar  
Time Table Coordinator

Dr. S. J. Honade  
HOD

Dr. K. N. Pathan  
Dean Academics

Dr. G. B. DONGRE  
PRINCIPAL

