



CSMSS
Chhatrapati Shahu Maharaj Shikshan Sanstha's
CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar 431 011
Ph. No. : (0240) 2646373, 9922668199, 2646350 Fax: (0240) 2646222
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Approved by AICTE New Delhi, DTE (Govt. of Maharashtra) and affiliated to Dr. BATU, Lonere (Raigad). **DTE Code: 2533**

Electronics Engineering (VLSI Design & Tech.) Department Gallery

Photograph	Name: Date: Details: in Short Para
	ATAL FDP 09/09/2024 One week ATAL sponsored FDP on “Semiconductor Design & Its Integration with Disruptive Technological Innovations” conducted at CSMSS’s CSCOE by EE(VLSI Design & Technology)
	Electronics skill development workshop for ACT students 07/05/2024 One Day “Electronics Skill Development workshop” for ACT students was conducted by EE(VLSI D&T) to enhance practical understanding about basics of electronics.



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Electronics skill development workshop for ECE students

08/05/2024

One Day “Electronics Skill Development workshop” for ECE students was conducted by EE(VLSI D&T) to enhance practical & hands-on skills related to electronics engineering and technology.



Electronics skill development workshop for EE(VLSI D&T) students

29/04/2024

Electronics Skill Development workshop in EE (VLSI D&T) Department was successfully conducted for first year students to enhance their practical understanding of electronics, fostering hands-on skills, problem-solving abilities, to better prepare them for future careers in engineering and technology.



Expert Talk By industry expert Mr. Vinay Sharma

10/09/2024

EE(VLSI D&T) Department organized an expert talk, inviting Mr. Vinay Sharma. Aim of the session was to explore Next Generation Nano Electronics Devices Circuits and its Application using EDA Simulation Tools.



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Done technology bootcamp

29/05/2024

Five day Drone Technology Bootcamp was organized by department of Electronic Engineering (VLSI Design & Technology) in collaboration with the esteemed NIELIT Aurangabad.



Guidance for Competitive Examination

09/10/2024

EE(VLSI D&T) Department conducted the program to create awareness about the GATE exam and guide second-year students on preparation strategies, exam structure, and career opportunities.



Teachers Day Celebration in VLSI Department

05/09/2024

A small event for celebrating Teachers Day was conducted at the VLSI Department to present gratitude to all the faculties for their work. Students organised quiz and fun games for Faculties and students.