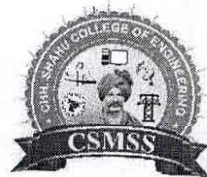




CSMSS

CHHATRAPATI SHAHU MAHARAJ SHIKSHAN SANSTHA'S
CHH. SHAHU COLLEGE OF ENGINEERING

Kanchanwadi, Paithan Road, Chhatrapati Sambhajnagar 431 002 (M.S)
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**REPORT OF
EXPERT TALK ON
"PERSONAL BRANDING AND LEADING YOUR WAY TO SUCCESS"**

Date: 21/03/2024

Time: 03:00 pm

Venue: CSMSS Chh. Shahu College of Engineering, Rajarshri Shahu Auditorium.

Goal: The aim of this interactive session is to explore the personal branding and leading your way to success.

Objective: The main objective of the program is as follows:

- The objective of the interactive session tailored for students and faculty members is to provide them with a comprehensive understanding of personal branding and way to success.
- The interactive session personalized for students and faculty members for equipping them with the tools and strategies necessary to excel in their academic pursuits and professional careers.
- Identify their unique strengths, skills, and passions and areas of expertise to establish a strong professional identity within their academic community.
- Analyze common challenges faced by students and faculty members in personal branding.

Benefits:

- Personal branding equips students with the tools and mindset necessary to navigate their future careers effectively.
- For faculty members, personal branding enhances their professional reputation and credibility within the academic community. By showcasing their expertise, research interests, and teaching philosophies, faculty members can attract opportunities for collaboration, publication, and recognition within their field.

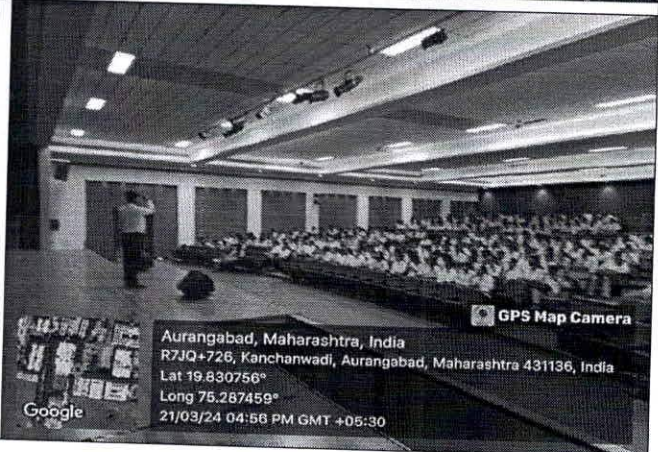
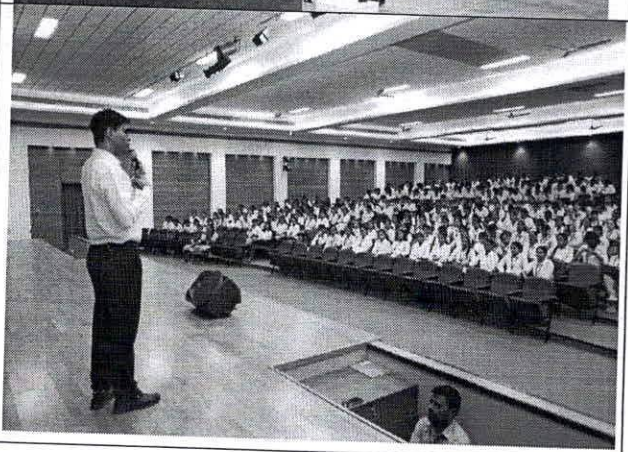
About Event:

- The interactive session was organized by CSMSS Chh. Shahu College of Engineering, Chh. Sambhajnagar under ISTE student chapter. The session started with lamp lighting ceremony followed by the felicitation of guest Prof. Dhananjay Singh by Dr. Ulhas B. Shinde, Principal, CSMSS Chh. Shahu College of Engineering, Chh. Sambhajnagar.
- Dr.S. J. Hondade has given brief information about ISTE(Indian Society for Technical Education)
- Dr.Ulhas B. Shinde addressed the students gathering and faculty in the workshop. He has explained the importance and significance of personal branding and leadership principles.
- An interactive session on **"PERSONAL BRANDING AND LEADING YOUR WAY TO SUCCESS"** was conducted by Prof. Dhananjay Singh (Associate Professor) NSBT, MGMU, Chh. Sambhajnagar for the FY, SY and TY students of CSMSS Chh. Shahu College of Engineering, Aurangabad. Prof. Dhananjay Singh, shared valuable insights into the unique strengths, skills, and passions to develop a distinct personal brand.
- He introduced the concept of personal branding and its significance in today's competitive world. Through interactive discussions and engaging activities, Prof. Dhananjay Singh encouraged students to reflect on their individual strengths, skills, and passions.
- He shared real-life examples and success stories to illustrate the importance of authenticity and uniqueness in personal branding.
- Prof. Singh emphasized the role of leadership in personal branding and encouraged students to take

ownership of their academic and professional journeys.

- The session concluded with a Q&A segment where students had the opportunity to seek clarification and share their insights on personal branding and leading your way to success.
- Ms. Vaidehi Salunke, Chairperson of ISTE Coordinator of the event presented the vote of thanks.
- Finally, the program was concluded with National anthem.
- Around 403 students and faculty members were present and benefitted through the program.

Glimpses of the Event



Dr. Shrikant J. Honade
HoD, EE (VLSI D & T) &
Institute Level ISTE Coordinator



Dr. U. B. Shinde
Principal



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REPORT OF

“INDUCTION PROGRAMME FOR SECOND YEAR STUDENTS”

Date: 12/08/2024

Time: 03:00 PM

Venue: CSMSS Chh. Shahu College of Engineering, VLSI D& T Department.

Goal: To familiarize B. Tech Second Year Students with the department's vision, mission, objectives, resources and the importance of aligning academic pursuits with industry demands and technological advancements.

Objective: The main objective of the program is as follows:

- To introduce students to the Institute and Department's vision and mission.
- To explain the Program Outcomes (PO), Program Specific Outcomes (PSO), and Program Educational Objectives (PEO).
- To provide information about department faculty, infrastructure, and achievements.
- To highlight opportunities for skill development and extracurricular activities.

Benefits:

The induction program offered several key benefits to the second-year B.Tech students in VLSI Design and Technology:

1. Students gained a clear understanding of the Institute and department's vision, mission, and the specific outcomes expected from their academic journey, helping them to align their efforts with the program's goals.
2. The program provided an opportunity for students to meet and interact with faculty members, establishing a rapport that can lead to better guidance, mentorship, and academic support throughout their studies.
3. By familiarizing students with the department's infrastructure, including smart classrooms and laboratories, the program ensured that they are well-equipped to utilize these resources effectively for their practical and theoretical learning.

About Event:

- The VLSI Design and Technology Department conducted an induction program for second-year B.Tech students to familiarize them with the department's vision, mission, objectives, resources and the importance of aligning academic pursuits with industry demands and technological advancements. The program was organized and led by Dr. S. J. Honade HoD, EE(VLSI D & T).
- Dr. S. J. Honade outlined the institutes and department's commitment to producing competent professionals equipped with the latest skills in VLSI design and related technologies. He discussed the long-term goals and aspirations that drive the department's curriculum and research initiatives.
- The session detailed the expected outcomes for students completing the program, including the specific knowledge and skills they will acquire. Dr. S. J. Honade explained how these outcomes align with the industry's needs and how they are integral to the students' academic and professional growth.
- An introduction of faculty members was provided, highlighting their qualifications, areas of expertise, and contributions to the field of VLSI design and technology. This session aimed to foster a connection between students and faculty, encouraging an open line of communication.
- Dr. S. J. Honade provided an overview of the department's infrastructure, including the projected smart classroom and laboratories. These facilities are designed to offer students hands-on experience with the tools and technologies they will encounter in the industry.

THE UNIVERSITY OF CHICAGO
DEPARTMENT OF CHEMISTRY

RESEARCH REPORT
No. 1234

BY
J. D. HARRIS

Submitted to the Department of Chemistry
on May 15, 1964

Approved by
Prof. J. H. Goldstein

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Chicago, Illinois 60637

Price \$10.00

- The department's recent achievements were showcased which includes the upcoming conduction of the ATAL Faculty Development Program (FDP) and the startup project under the Pre-incubation program by DBATU Forum of Innovation, Incubation & Enterprise (DFIIE) Cohort 2.0 - स्टार्ट अप नवांकुर.
- Details were provided about the upcoming Drone Bootcamp, which is designed to equip students with practical skills in drone technology a growing field with significant industry demand.
- Dr. S. J. Honade has given details of the Electronics Skill Development Workshop. The successful event of the Electronics Skill Development Workshop for VLSI, ACT & ECE Students, aimed at enhancing the practical skills of students in VLSI, Advanced Communication Technology (ACT), and Electronics and Communication Engineering (ECE).
- The induction program successfully oriented the students with the department's objectives and the resources available to them. It set a positive tone for the academic year and motivated students to actively participate in the various opportunities presented to them.
- Around 51 students and all faculty members of department of EE(VLSI D & T) were present for the induction program.

Glimpses of the Event



S. J. Honade

Dr. Shrikant J. Honade
HoD, EE (VLSI D & T)



U. B. Shinde

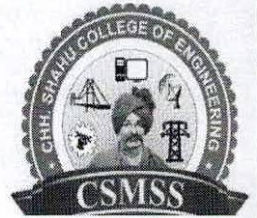
Dr. U. B. Shinde
Principal





CSMSS

Chh. Shahu College of Engineering, Chh. Sambhajinagar (Aurangabad)



Report of Workshop on “MATLAB, Simulink and its Application”

Date: 15/03/2024

Time: 01:30 pm

Venue: CSMSS Chh. Shahu College of Engineering, Rajarshi Shahu Auditorium.

Goal: The aim of this interactive session is to explore the capabilities, applications, and benefits of MATLAB and Simulink in various engineering domains.

Objective: The main objective of the program is as follows:

- To make students and faculty members aware about latest toolboxes and facilities available in MATLAB and Simulink.
- To explore the industrial applications of MATLAB and Simulink.
- To demonstrate the use of various Apps for AI, ML, DL, Signal and Image Processing in MATLAB.

Benefits:

- Faculty members & students are motivated and aware about recent features of MATLAB and Simulink.
- Students have practically experienced the use of AI, ML, DL, Signal and Image Processing apps in MATLAB and its application.

About Workshop:

- The workshop was organized by department of Electronic Engineering (VLSI Design and Technology). The event started with lamp lightening ceremony followed by the felicitation of guest Mr. Ankit Kumar by Dr. Ulhas B. Shinde, Principal, CSMSS Chh. Shahu College of Engineering, Chh. Sambhajinagar.
- Dr. Ulhas B. Shinde addressed the students gathering and faculty in the workshop. He has explained the importance and significance of MATLAB and Simulink for engineers in various domains.
- An interactive session on “MATLAB, Simulink and Its Application” was conducted by Mr. Ankit Kumar, Application Engineer, Design Tech System Pvt. Ltd, Pune for the TY, SY students of CSMSS Chh. Shahu College of Engineering, Aurangabad. Mr. Amit Kumar, shared valuable insights into the latest up gradation in MATLAB and its applications in different domain.
- Mr. Ankit Kumar had also given the practical demonstration on MATLAB and Simulink interface; basic commands and functions to familiarize participants, discussions and demonstrations on how MATLAB and Simulink are utilized in various engineering disciplines, including mechanical, electrical, Electronics, AL and ML, Civil engineering and also discussed on techniques for data analysis, manipulation, and visualization using MATLAB.
- A significant portion of the session was dedicated to teaching and learning with MATLAB, computational analysis, confusion matrix, machine learning and deep learning using MATLAB and Mr. Ankit Kumar elucidated the MATLAB capabilities in:
 - Data analysis
 - Graphics

- Algorithms development
- APP development
- Parallel computing
- Dr. Shrikant J. Honade, HoD EE(VLSI D &T) and overall coordinator of the event presented the vote of thanks.
- Finally, the program was concluded with National anthem.
- Around 255 students and 28 faculty members were present and benefitted through the program.

Glimpses of the Event



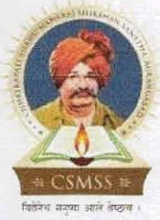
Dr. Shrikant J. Honade

Dr. Shrikant J. Honade
Event Coordinator



Dr. U. B. Shinde

Dr. U. B. Shinde
Principal



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REPORT OF EXPERT TALK ON

“NEXT GENERATION NANO ELECTRONICS DEVICES CIRCUITS AND ITS APPLICATION USING EDA SIMULATION TOOLS”

Date: 10/09/2024

Time: 03:00 pm

Venue: CSMSS Chh. Shahu College of Engineering, Smart Lab B-406.

Goal: The aim of this Expert interactive session is to explore Next Generation Nano Electronics Devices Circuits and its Application using EDA Simulation Tools.

Objective: The main objective of the program is as follows:

- To provide students with insights into the field of Nano Electronics Devices and Circuits, focusing on the next-generation advancements and practical applications of these technologies using EDA (Electronic Design Automation) simulation tools.

Benefits:

- **Exposure to Industry-Relevant Tools:** The hands-on experience with NI2logic and Microwind software gave students practical skills with industry-standard EDA simulation tools, preparing them for real-world applications in VLSI design and nano-electronics.
- **Enhanced Understanding of Nano Electronics:** The expert lecture provided students with in-depth knowledge of next-generation nano-electronic devices and circuits, crucial for advancements in modern technology such as IoT and high-performance computing.
- **Skill Development for Future Projects:** Students gained valuable simulation and design skills, which will be instrumental in their academic projects and professional careers in fields like semiconductor technology and advanced electronics.

About Event:

- The Expert Talk was organized by EE(VLSI D&T) Department, CSMSS Chh. Shahu College of Engineering, Chh. Sambhajinagar. The session started with the felicitation of guest Mr. Vinay Sharma by Dr. S. J. Honade, HOD EE(VLSI D&T) CSCOE, Chh. Sambhajinagar.
- Dr. S. J. Honade has given brief information about Expert Talk on Next Generation Nano Electronics Devices Circuits and Its Application Using EDA Simulation Tools.
- An interactive session on “Next Generation Nano Electronics Devices Circuits and Its Application Using EDA Simulation Tools” was conducted by Mr. Vinay Sharma for the SY students of EE (VLSI D & T) Department.
- The expert session commenced with a detailed introduction to Nano Electronics Devices, where Mr. Sharma elaborated on the evolution of electronics at the nanoscale, highlighting the importance of miniaturization in modern electronics. He covered various aspects of nano-electronic devices, including:
 - Fundamental concepts of Nano Electronics.
 - Types of Nano Electronics devices and their applications.
 - Challenges and future prospects of nano-scale electronics design.
- Mr. Sharma also introduced the students to circuit-level design using modern EDA tools, discussing their importance in industry and research.

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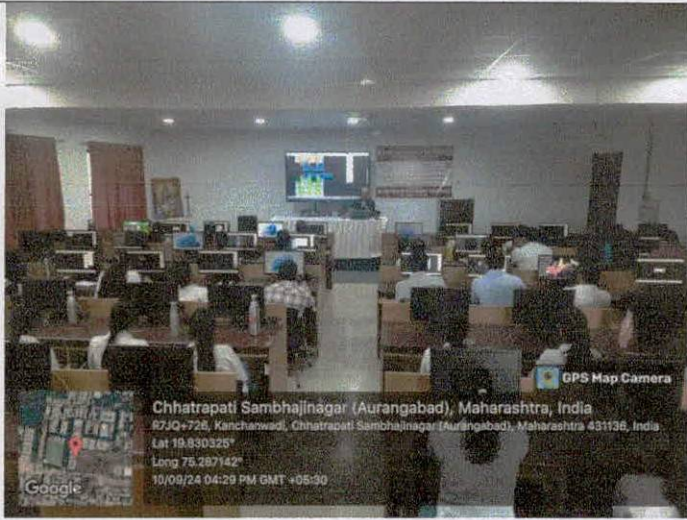
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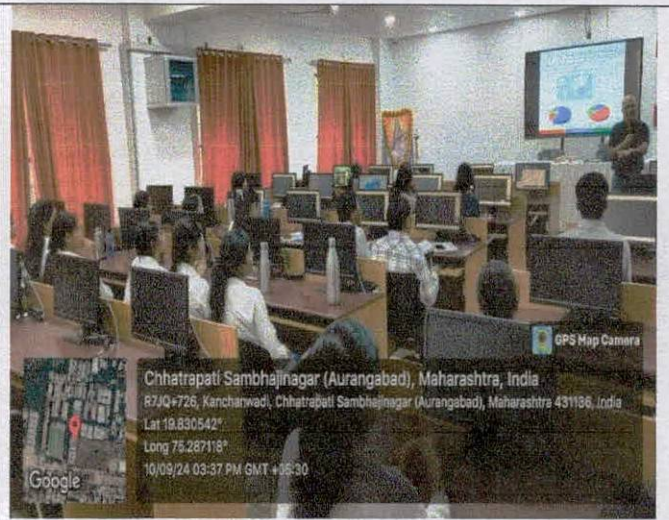
- A key highlight of the session was the hands-on practical experience where students worked with two prominent EDA simulation tools:
 - **NI2logic Software:**
Students were guided through the steps of creating and simulating nano-electronic circuits using this advanced tool. Mr. Sharma explained its features and showed how it can be used for complex circuit designs in both academic and industrial applications.
 - **Microwind Software:**
The hands-on session also included Microwind software, where students learned how to simulate various nano-electronic devices, understand layout designs, and analyze circuit performance.
- The session witnessed active participation from second-year B.Tech students, who engaged with the tools and asked insightful questions about the practical applications of Nano Electronics.
- Dr. S. J. Honade delivered the vote of thanks and presented certificate of appreciation and letter of thanks to Mr. Vinay Sharma.
- The session concluded with a Q & A segment where students had the opportunity to seek clarification and share their insights on EDA tools.
- Around 52 students and faculty members were present and benefitted through the program.

Glimpses of the Event

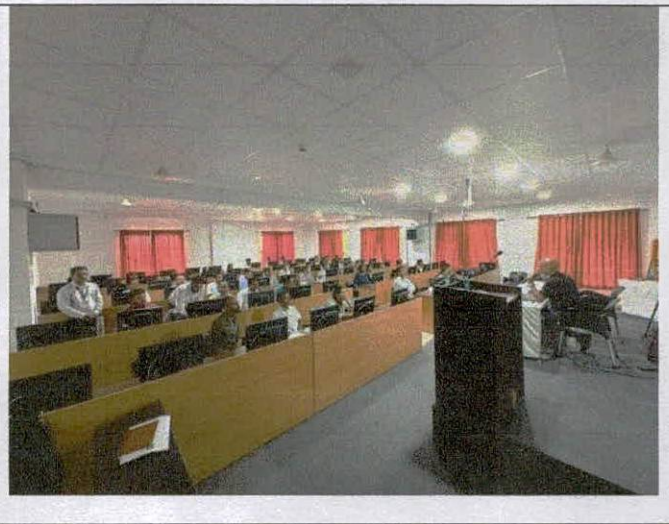
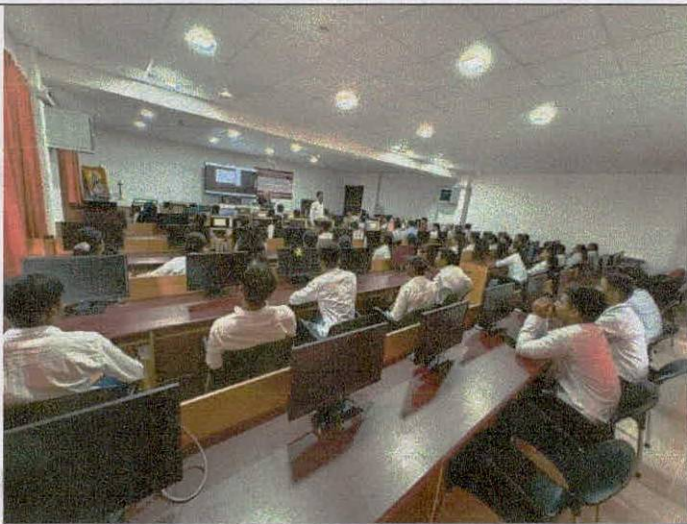




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 Long 75.287142°
 10/09/24 04:29 PM GMT +05:30



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 Long 75.287118°
 10/09/24 03:37 PM GMT +05:30



Dr. Shrikant J. Honade

Dr. Shrikant J. Honade
 HoD, EE (VLSI D & T)



Dr. U. B. Shinde

Dr. U. B. Shinde
 Principal



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REPORT OF

“ORIENTATION PROGRAMME FOR FIRST YEAR STUDENTS”

Date: 27/09/2024

Time: 03:00 PM

Venue: CSMSS Chh. Shahu College of Engineering, VLSI D& T Department.

Goal: To familiarize B. Tech First Year Students with the department's vision, mission, objectives, resources and the importance of aligning academic pursuits with industry demands and technological advancements.

Objective: The main objective of the program is as follows:

- To introduce students to the Institute and Department's vision and mission.
- To explain the Program Outcomes (PO), Program Specific Outcomes (PSO), and Program Educational Objectives (PEO).
- To provide information about department faculty, infrastructure, and achievements.
- To highlight opportunities for skill development and extracurricular activities.

Benefits:

The induction program offered several key benefits to the First-year B.Tech students in VLSI Design and Technology:

1. Students gained a clear understanding of the Institute and department's vision, mission, and the specific outcomes expected from their academic journey, helping them to align their efforts with the program's goals.
2. The program provided an opportunity for students to meet and interact with faculty members, establishing a rapport that can lead to better guidance, mentorship, and academic support throughout their studies.
3. By familiarizing students with the department's infrastructure, including smart classrooms and laboratories, the program ensured that they are well-equipped to utilize these resources effectively for their practical and theoretical learning.

About Event:


- The VLSI Design and Technology Department conducted an induction program for First-year B.Tech students to familiarize them with the department's vision, mission, objectives, resources and the importance of aligning academic pursuits with industry demands and technological advancements. The program was organized and led by Dr. S. J. Honade HoD, EE(VLSI D & T).
- Dr. S. J. Honade outlined the institutes and department's commitment to producing competent professionals equipped with the latest skills in VLSI design and related technologies. He discussed the long-term goals and aspirations that drive the department's curriculum and research initiatives.
- The session detailed the expected outcomes for students completing the program, including the specific knowledge and skills they will acquire. Dr. S. J. Honade explained how these outcomes align with the industry's needs and how they are integral to the students' academic and professional growth.
- An introduction of faculty members was provided, highlighting their qualifications, areas of expertise, and contributions to the field of VLSI design and technology. This session aimed to foster a connection between students and faculty, encouraging an open line of communication.
- Dr. S. J. Honade provided an overview of the department's infrastructure, including the projected

smart classroom and laboratories. These facilities are designed to offer students hands-on experience with the tools and technologies they will encounter in the industry.

- The department's recent achievements were showcased which includes the upcoming conduction of the ATAL Faculty Development Program (FDP) and the startup project under the Pre-incubation program by DBATU Forum of Innovation, Incubation & Enterprise (DFIIE) Cohort 2.0 - स्टार्ट अप नवांकर.
- Details were provided about the upcoming Drone Bootcamp, which is designed to equip students with practical skills in drone technology a growing field with significant industry demand.
- Dr. S. J. Honade has given details of the Electronics Skill Development Workshop. The successful event of the Electronics Skill Development Workshop for VLSI, ACT & ECE Students, aimed at enhancing the practical skills of students in VLSI, Advanced Communication Technology (ACT), and Electronics and Communication Engineering (ECE).
- The induction program successfully oriented the students with the department's objectives and the resources available to them. It set a positive tone for the academic year and motivated students to actively participate in the various opportunities presented to them.
- Around 66 students and all faculty members of department of EE(VLSI D &T) were present for the induction program.

Glimpses of the Event




HoD, EE (VLSI D & T)
Dr. Shrikant J. Honade
 Head of Department
 Electronics Engineering
 VLSI Design & Technology




Principal



Dr. Shankant J. Honde
Head of Department
Electronics Engineering
(VLSI Design & Technology)



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REPORT OF ONE DAY EVENT ON
“TEACHERS DAY CELEBRATION”

Date: 05/09/2024

Time: 03:15 PM

Venue: CSMSS CSCOE, Seminar Hall, A Building, VLSI (D & T) Department.

Goal: The aim of this event is to honor and recognize the contributions of teachers to society.

Objective: The main objective of the program is as follows:

1. To thank the teachers for their dedication, guidance, and hard work throughout the year.
2. To honor the contributions of teachers to society and acknowledge their vital role in students' academic and personal growth.
3. To fosters a sense of community and unity within the department by bringing students and teachers together in celebration.
4. To enhance the soft skills of the students.

Benefits: Celebrating Teachers Day offers several benefits, including:

1. **Motivation for teacher:** Acknowledging the efforts taken by the teachers inspires them to continue their work with greater enthusiasm and dedication.
2. **Strengthening Teacher-Student Relationships:** Students express their appreciation and respect for their teachers, resulting in creation of a positive and supportive classroom environment.
3. **Fostering a Positive Culture:** It provides an opportunity to reflect on the academic and social progress made.
4. **Encourages Collaboration:** Teachers' Day brings students, teachers, and school staff together, fostering a sense of community and teamwork.
5. **Enhancing Students' Character Development:** Teachers' Day emphasizes the values of respect, gratitude, and the importance of education, shaping students' character.
6. **Improves Classroom Dynamics:** Celebrating Teachers' Day can positively influence the learning environment by reinforcing mutual respect and collaboration.
7. **Emphasizes Education's Role in Society:** Celebrating Teachers' Day brings attention to the vital role education plays in personal, social, and national development.

About Event:

- The one-day event was organized by department of Electronic Engineering (VLSI Design and Technology).
- The event started with lamp lightening ceremony by Hon. Dr. S. J. Honade HoD EE (VLSI D & T), Dr. J. R. Shinde, Prof. T. A. Mohije, Prof. I. M. Palkar and all faculty members of department of EE (VLSI D & T).
- Dr. S. J. Honade HoD EE (VLSI D & T), address the audience about roles and responsibilities of a Teacher in society.
- One student from Second Year Electronics Engineering (VLSI D & T), given speech on importance of Teacher's day.
- Students from Second Year Electronics Engineering (VLSI D & T) felicitated all the faculty members.
- At the end of the event, students conducted quiz competition for faculty members.

MEMORANDUM FOR THE RECORD

DATE: 10/10/72

TO: SAC, NEW YORK

FROM: SA [Name], NEW YORK

SUBJECT: [Subject]

RE: [Subject]

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- The workshop was coordinated by Prof. G. G. Patil.
- A vote of gratitude was presented by Prof. T. A. Mohije, Class Teacher SY EE (VLSI D & T).
- Around 52 students were present through the program.

Glimpses of the Event



(Signature)

Dr. Shrikant J. Honade
HOD, EE (VLSI D & T) &
Institute Level ISTE Coordinator



(Signature)

Dr. U. B. Shinde
Principal



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Email :shahuengg@gmail.com, principal@csmsseengg.org Website : www.csmsseengg.org



REPORT OF ONE-DAY WORKSHOP ON

“ELECTRONIC SKILL DEVELOPMENT”

Date: 07/05/2024

Time: 10:00 AM

Venue: CSMSS Chh. Shahu College of Engineering, VLSI D& T Department.

Goal: The aim of the workshop for B. Tech Electronics and Communication (Advanced Communication Technology) first-year students is to enhance their practical understanding of electronics, fostering hands-on skills, problem-solving abilities, to better prepare them for future careers in engineering and technology.

Objective: The main objective of the program is as follows:

- To familiarize students with the fundamental concepts of electronics, including components, circuits, and systems.
- To provide practical, hands-on activities for students to apply theoretical knowledge and develop practical skills in electronic circuit design, construction, and troubleshooting.
- To teach students about various electronic components such as resistors, capacitors, diodes, transistors, and integrated circuits, including their functions and applications.
- To introduce students to basic circuit design principles and analysis techniques, enabling them to design simple electronic circuits and understand their behavior.
- To teach students how to use prototyping tools like breadboards to quickly build and test electronic circuits without soldering, facilitating rapid prototyping and experimentation.

Benefits:

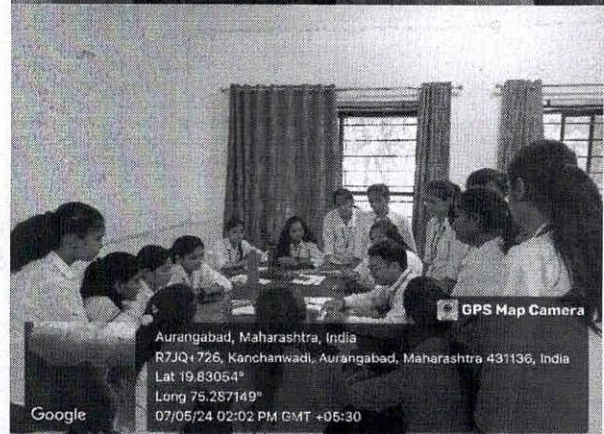
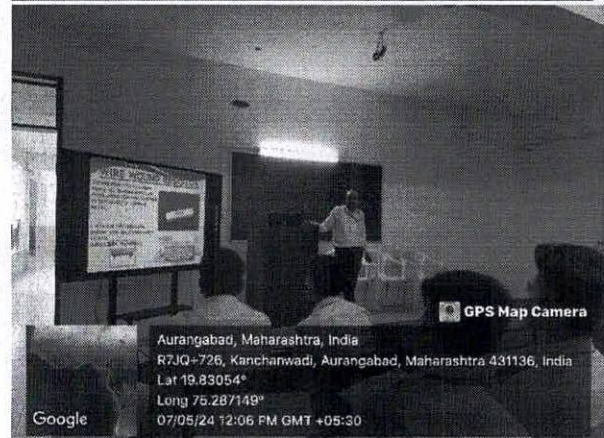
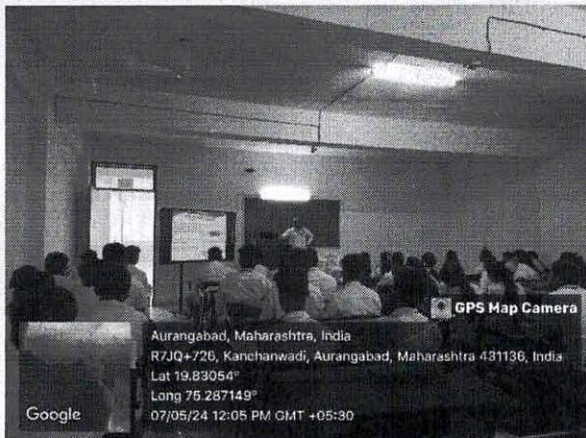
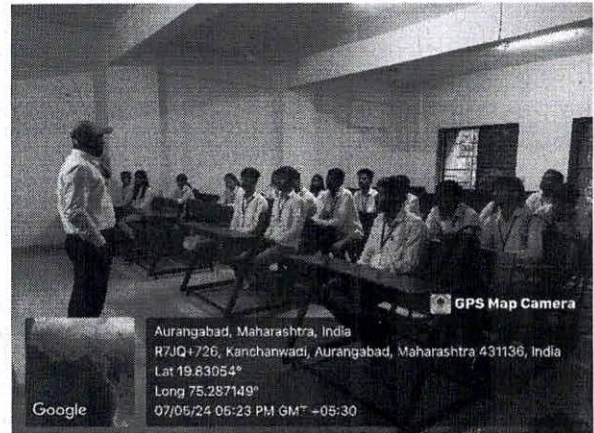
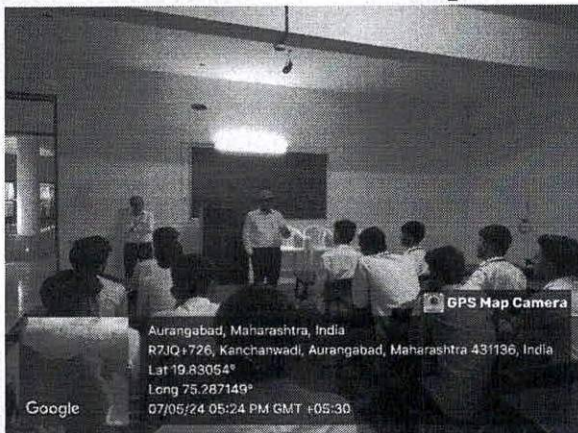
- Students gain a deeper understanding of electronics principles and concepts, which can complement their academic studies and improve their performance in related courses.
- Engaging in practical activities allows students to apply theoretical knowledge in real-world scenarios, reinforcing learning and developing practical skills essential for future endeavors.
- The workshop offers opportunities for students to develop practical skills in circuit design, prototyping, troubleshooting which are valuable in various academic and professional surroundings.

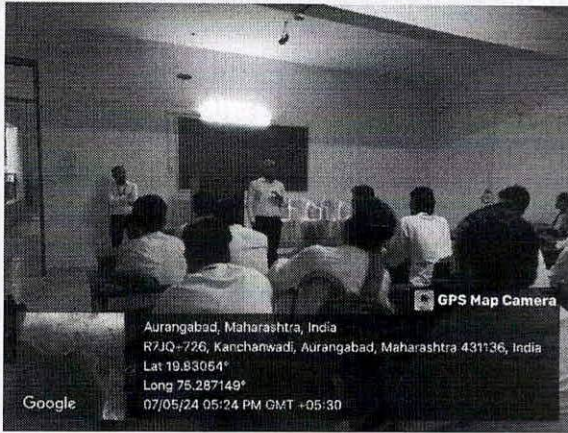
About Event:

- Dr, S. J. Honade has given the introduction and brief overview of one day workshop on Electronic Skill Development.
- The workshop was divided into four sessions:
 - 1) Understanding Electronic Components
 - 2) Hands-on activity: Building simple circuits on breadboards to demonstrate concepts
 - 3) Introduction to Digital Electronics
 - 4) Practical applications of basic electronics
- Prof. Ganesh N. Dhengale delivered an insightful lecture on Understanding Electronic Components, elucidating the fundamentals and functions of resistors, capacitors, LEDs, and more.
- Hands-on sessions covering the construction of simple circuits on breadboards and a demonstration of digital electronics were led by Prof. Ganesh G. Patil and Prof. Satish H. Jadhav.
- In the hands-on activity, participants explored darkness detection circuitry, fire alarm circuit design, basic gate implementation, short circuit protection mechanisms, and delved into a basic project utilizing ICs.
- At the end of workshop, a competition was organized where participants formed groups of five students. Each group was given a task for building a basic circuit. The appreciation of two best-performer groups were declared in the form of Winner and Runner-up.

- The workshop was coordinated by Prof. Ganesh G. Patil, Prof. Ganesh N. Dhengale and Prof. Satish H. Jadhav.
- A vote of gratitude was presented by Dr. Shrikant J. Honade, HoD EE (VLSI D &T) and Institute Level ISTE Coordinator.
- Around 35 students were present and benefitted through the workshop.

Glimpses of the Event





GPS Map Camera

Aurangabad, Maharashtra, India
R7JQ+726, Kanchanwadi, Aurangabad, Maharashtra 431136, India
Lat: 19.83054°
Long 75.287149°
07/05/24 05:24 PM GMT +05:30

Google



GPS Map Camera

Aurangabad, Maharashtra, India
R7JQ+726, Kanchanwadi, Aurangabad, Maharashtra 431136, India
Lat: 19.83054°
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07/05/24 12:05 PM GMT +05:30

Google

Dr. Shrikant J. Honade
HoD, EE (VLSI D & T) &
Institute Level ISTE Coordinator



Dr. U. B. Shinde
Principal





REPORT OF ONE-DAY WORKSHOP ON
“ELECTRONIC SKILL DEVELOPMENT”

Date: 08/05/2024

Time: 10:00 AM

Venue: CSMSS Chh. Shahu College of Engineering, VLSI D& T Department.

Goal: The aim of the workshop for B. Tech Electronics & Computer Engineering first-year students is to enhance their practical understanding of electronics, fostering hands-on skills, problem-solving abilities, to better prepare them for future careers in engineering and technology.

Objective: The main objective of the program is as follows:

- To familiarize students with the fundamental concepts of electronics, including components, circuits, and systems.
- To provide practical, hands-on activities for students to apply theoretical knowledge and develop practical skills in electronic circuit design, construction, and troubleshooting.
- To teach students about various electronic components such as resistors, capacitors, diodes, transistors, and integrated circuits, including their functions and applications.
- To introduce students to basic circuit design principles and analysis techniques, enabling them to design simple electronic circuits and understand their behavior.
- To teach students how to use prototyping tools like breadboards to quickly build and test electronic circuits without soldering, facilitating rapid prototyping and experimentation.

Benefits:

- Students gain a deeper understanding of electronics principles and concepts, which can complement their academic studies and improve their performance in related courses.
- Engaging in practical activities allows students to apply theoretical knowledge in real-world scenarios, reinforcing learning and developing practical skills essential for future endeavors.
- The workshop offers opportunities for students to develop practical skills in circuit design, prototyping, troubleshooting which are valuable in various academic and professional surroundings.

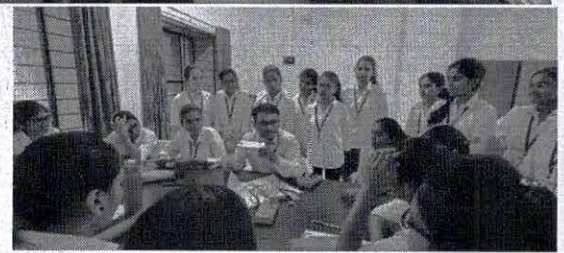
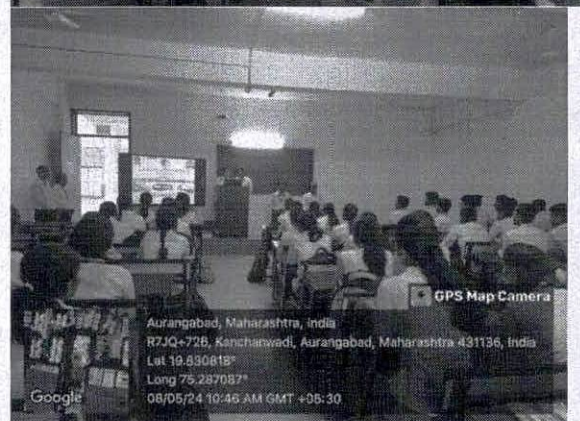
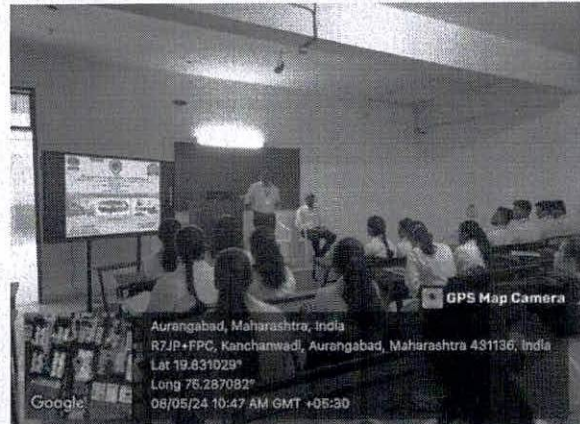
About Event:

- Dr. S. J. Honade has given the introduction and brief overview of one day workshop on Electronic Skill Development.
- Dr. D. L. Bhuyar was given an overview of carrier opportunities available in electronics and computer engineering.
- The workshop was divided into four sessions:
 - 1) Understanding Electronic Components
 - 2) Hands-on activity: Building simple circuits on breadboards to demonstrate concepts
 - 3) Introduction to Digital Electronics
 - 4) Practical applications of basic electronics
- Prof. Ganesh N. Dhengale delivered an insightful lecture on Understanding Electronic Components, elucidating the fundamentals and functions of resistors, capacitors, LEDs, and more.
- Hands-on sessions covering the construction of simple circuits on breadboards and a demonstration of digital electronics were led by Prof. Ganesh G. Patil and Prof. Satish H. Jadhav.
- In the hands-on activity, participants explored darkness detection circuitry, fire alarm circuit design, basic gate implementation, short circuit protection mechanisms, and delved into a basic project utilizing ICs.
- At the end of workshop, a competition was organized where participants formed groups of five

students. Each group was given a task for building a basic circuit. The appreciation of two best-performer groups were declared in the form of Winner and Runner-up.

- The workshop was coordinated by Prof. Ganesh G. Patil, Prof. Ganesh N. Dhengale and Prof. Satish H. Jadhav.
- A vote of gratitude was presented by Dr. Shrikant J. Honade, HoD EE (VLSI D &T) and Institute Level ISTE Coordinator.
- Around 57 students were present and benefitted through the workshop.

Glimpses of the Event





J. Honade

Dr. Shrikant J. Honade
HoD, EE (VLSI D & T) &
Institute Level ISTE Coordinator



U. B. Shinde

Dr. U. B. Shinde
Principal





REPORT OF ONE-DAY WORKSHOP ON

“ELECTRONIC SKILL DEVELOPMENT ”

Date: 29/04/2024

Time: 10:00 AM

Venue: CSMSS Chh. Shahu College of Engineering, VLSI D& T Department.

Goal: The aim of the workshop for B. Tech Electronics Engineering (VLSI Design and Technology) first year students is to enhance their practical understanding of electronics, fostering hands-on skills, problem-solving abilities, to better prepare them for future careers in engineering and technology.

Objective: The main objective of the program is as follows:

- To familiarize students with the fundamental concepts of electronics, including components, circuits, and systems.
- To provide practical, hands-on activities for students to apply theoretical knowledge and develop practical skills in electronic circuit design, construction, and troubleshooting.
- To teach students about various electronic components such as resistors, capacitors, diodes, transistors, and integrated circuits, including their functions and applications.
- To introduce students to basic circuit design principles and analysis techniques, enabling them to design simple electronic circuits and understand their behavior.
- To teach students how to use prototyping tools like breadboards to quickly build and test electronic circuits without soldering, facilitating rapid prototyping and experimentation.

Benefits:

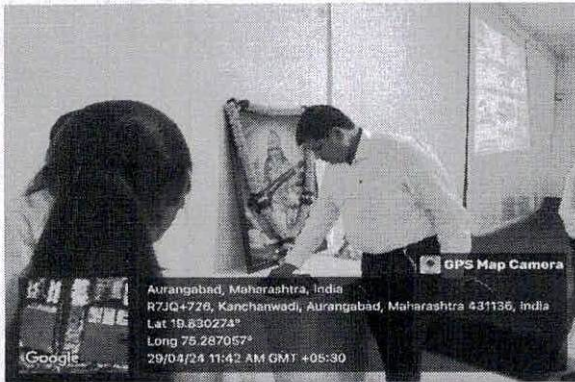
- Students gain a deeper understanding of electronics principles and concepts, which can complement their academic studies and improve their performance in related courses.
- Engaging in practical activities allows students to apply theoretical knowledge in real-world scenarios, reinforcing learning and developing practical skills essential for future endeavors.
- The workshop offers opportunities for students to develop practical skills in circuit design, prototyping, troubleshooting which are valuable in various academic and professional surroundings.

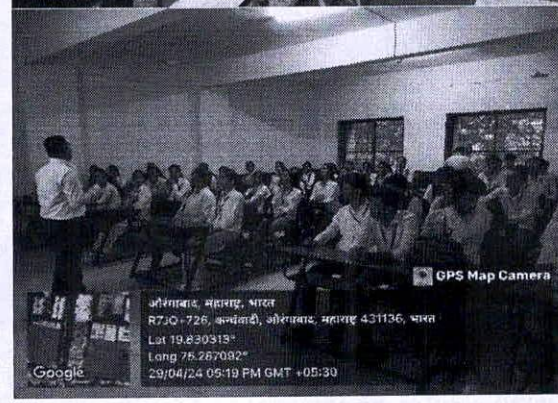
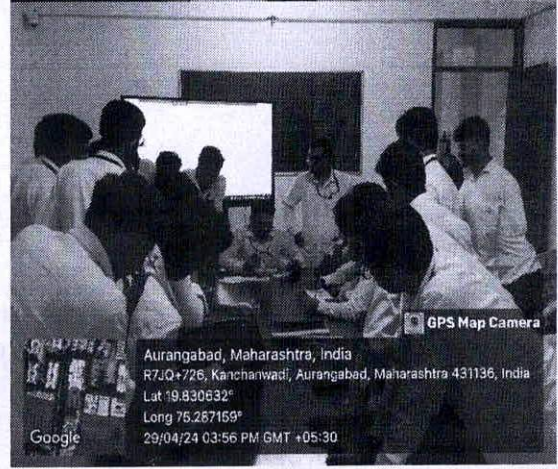
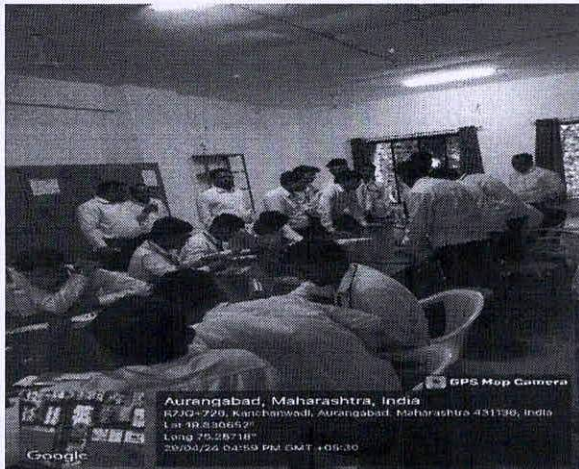
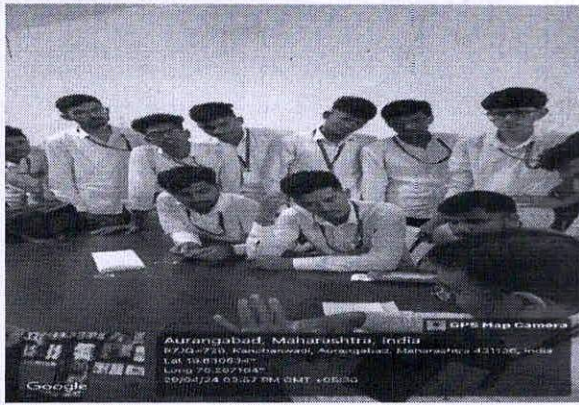
About Event:

- The one day workshop was organized by department of Electronic Engineering (VLSI Design and Technology). The event started with lamp lightening ceremony by Hon. Dr. Ulhas B. Shinde, Principal, CSMSS Chh. Shahu College of Engineering, Chh. Sambhajnagar, Dr. D. L. Bhuyar Vice Principal, Dr. S. J. Honade HoD EE (VLSI D&T) and Prof B. S. Deshmukh HoD FY Engg.
- Dr, S. J. Honade has given the introduction and brief overview of one day workshop on Electronic Skill Development.
- Dr.Ulhas B. Shinde addressed the students gathering and faculty in the workshop. He has explained the importance and significance of workshop: Electronics Skills requirement and various opportunities for students of VLSI branch.
- The workshop was divided into four sessions:
 - 1) Understanding Electronic Components
 - 2) Hands-on activity: Building simple circuits on breadboards to demonstrate concepts
 - 3) Introduction to Digital Electronics
 - 4) Practical applications of basic electronics
- Prof. Ganesh N. Dhengale delivered an insightful lecture on Understanding Electronic Components, elucidating the fundamentals and functions of resistors, capacitors, LEDs, and more.

- Hands-on sessions covering the construction of simple circuits on breadboards and a demonstration of digital electronics were led by Prof. Ganesh G. Patil and Prof. Satish H. Jadhav.
- In the hands-on activity, participants explored darkness detection circuitry, fire alarm circuit design, basic gate implementation, short circuit protection mechanisms, and delved into a basic project utilizing ICs.
- At the end of workshop, a competition was organized where participants formed groups of five students. Each group was given a task for building a basic circuit. The appreciation of two best performer group were declared in the form of Winner and Runner-up.
- The workshop was coordinated by Prof. Ganesh G. Patil, Prof. Ganesh N. Dhengale and Prof. Satish H. Jadhav.
- A vote of gratitude was presented by Dr. Shrikant J. Honade, HoD EE (VLSI D &T) and Institute Level ISTE Coordinator.
- Around 50 students were present and benefitted through the workshop.

Glimpses of the Event





Dr. Shrikant J. Honade

Dr. Shrikant J. Honade
 HoD, EE (VLSI D & T) &
 Institute Level ISTE Coordinator



Dr. U. B. Shinde

Dr. U. B. Shinde
 Principal





REPORT OF ONE DAY EVENT ON

“ENGINEERS DAY CELEBRATION”

Date: 18/09/2024

Time: 03:15 PM

Venue: CSMSS CSCOE, Seminar Hall A Building, VLSI (D & T) Department.

Goal: The aim of this event is to recognize and honor the contributions of engineers to society and technological advancement.

Objective: The main objective of the program is as follows:

1. To honor the contributions of engineers to society and promote awareness of their vital role in technological advancement and infrastructure development.
2. To inspire future generations to pursue careers in engineering and to recognize the importance of innovation in addressing global challenges.
3. To enhance the soft skills and technical knowledge of the students.

Benefits: Celebrating Engineers Day offers several benefits, including:

1. **Recognition of Contributions:** It highlights the vital role engineers play in society, showcasing their innovations and problem-solving capabilities.
2. **Inspiration for Future Generations:** It encourages students and young professionals to consider careers in engineering, fostering interest in STEM fields.
3. **Promotion of Collaboration:** It creates opportunities for engineers to connect, share ideas, and collaborate on projects, leading to greater innovation.
4. **Public Awareness:** It educates the public about the importance of engineering in everyday life and its impact on quality of life and economic development.
5. **Professional Development:** Events and discussions during the celebration can provide networking opportunities and insights into emerging trends and technologies in engineering.
6. **Civic Engagement:** It fosters a sense of community and pride among engineers, encouraging them to engage in outreach and public service initiatives.

About Event:

- The one day event was organized by department of Electronic Engineering (VLSI Design and Technology). The event started with lamp lightening ceremony by Hon. Dr. S. J. Honade HoD EE (VLSI D & T), Dr. J. R. Shinde, Prof. Tushar Mohije, Prof. P.R.Bhusari and all faculty members of department of EE (VLSI D & T).
- Dr. S. J. Honade HoD EE (VLSI D & T), address the audience about roles and responsibilities of an Engineers in society. He said, Engineers should always accept the challenges with positive mindset and work on innovative projects/ideas in order to provide ethical, sustainable and optimal solutions and motivated students to work on live projects.
- Dr. Jitesh R. Shinde addressed the students. He has explained the importance and significance of various schemes launched by government of India Vatsalya Scheme its importance and benefits to become successful entrepreneur.
- One student from Second Year Electronics Engineering (VLSI D & T) given speech on importance of Engineer's day.

- Two students Aditya and Rushikesh from Second Year Electronics Engineering (VLSI D & T) given insightful presentation on importance of VLSI and trends in Industry.
- At the end of workshop, a quiz competition was organized where participants formed groups of six students. Each group was given a task to answer for 10 MCQ those who will answer more they will be the winner. The appreciation of one best performer group were declared in the form of Winner.
- The workshop was coordinated by Prof. Pankaj R. Bhusari and Prof. Ganesh G. Patil.
- A vote of gratitude was presented by Prof. Tushar Mohije, Class Teacher EE (VLSI D & T).
- Around 55 students were present and benefitted through the program.

Glimpses of the Event



Dr. Shrikant J. Honade

Dr. Shrikant J. Honade
HOD, EE (VLSI D & T) &
Institute Level ISTE Coordinator



Dr. U. B. Shinde

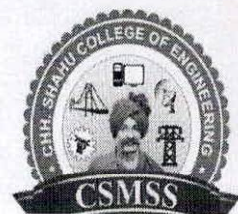
Dr. U. B. Shinde
Principal





CSMSS

Chh. Shahu College of Engineering,
Chh. Sambhajinagar (Aurangabad)



Report of
Expert Talk on “Emerging Trends in VLSI Design”
Followed by
Live Video-conferencing Program of “Laying of foundation Stone of 3
Semiconductor Facilities by Hon’ble Prime Minister of India Shri. Narendra
Modi”

Date: 13/03/2024

Time: 09:30 am

Venue: CSMSS Chh. Shahu College of Engineering, Seminar Hall A-203.

Goal: To create awareness among students about opportunities and importance of Semiconductor and latest trends in VLSI Design and Technology.

Objective: The main objective of the program are as follows:

- To make students and faculty members aware about latest trends and scope of VLSI Design.
- To make students and faculty members aware about Semiconductor mission of India.
- To make students and faculty members aware about importance of Semiconductor Fab Labs.
- To make students and faculty members aware about opportunities and initiatives of Indian government in chip fabrication.

Benefits:

- Faculty members & students are motivated and aware about recent technological trends and semiconductor mission of India.
- Students are motivated for imbibing the skills in order to grab the opportunities in future and also recent initiatives of Indian government in chip fabrication.

About Program:

- An expert talk by Dr. Shrikant J. Honade, HoD Electronics Engineering (VLSI Design & Technology) on "Recent Trends in VLSI and Awareness about Semiconductor Mission of India" was organized for the students of CSMSS Chh Shahu College of Engineering, Aurangabad. Dr. Honade, shared valuable insights into the latest advancements in Very Large Scale Integration (VLSI) technology and the significance of the Semiconductor Mission of India.
- A significant portion of the talk was dedicated to creating awareness about the Semiconductor Mission of India. Dr. Honade elucidated the mission's objectives, aiming to position India as a global leader in semiconductor manufacturing and design. Students learned about the mission's potential impact on the industry and the opportunities it offers. Dr. Honade emphasized the numerous opportunities available to students within the semiconductor ecosystem.
- Hon'ble Prime Minister Shri. Narendra Modi, along with other ministers, participated in a significant YouTube Video Conference to lay the foundation stone of three semiconductor facilities in India. This landmark event is a pivotal step towards enhancing the country's semiconductor manufacturing capabilities, supporting India's vision of self-reliance in technology and electronics.
- The three semiconductor projects launched on Wednesday 13th March 2024 include India's first Fab facility at Dholera in Gujarat, Outsourced Semiconductor Assembly and Test (OSAT) facility at Sanand in the state, and an OSAT facility in Morigaon, Assam.

