



CSMSS
Chhatrapati Shahu Maharaj Shikshan Sanstha's
CHH. SHAHU COLLEGE OF ENGINEERING
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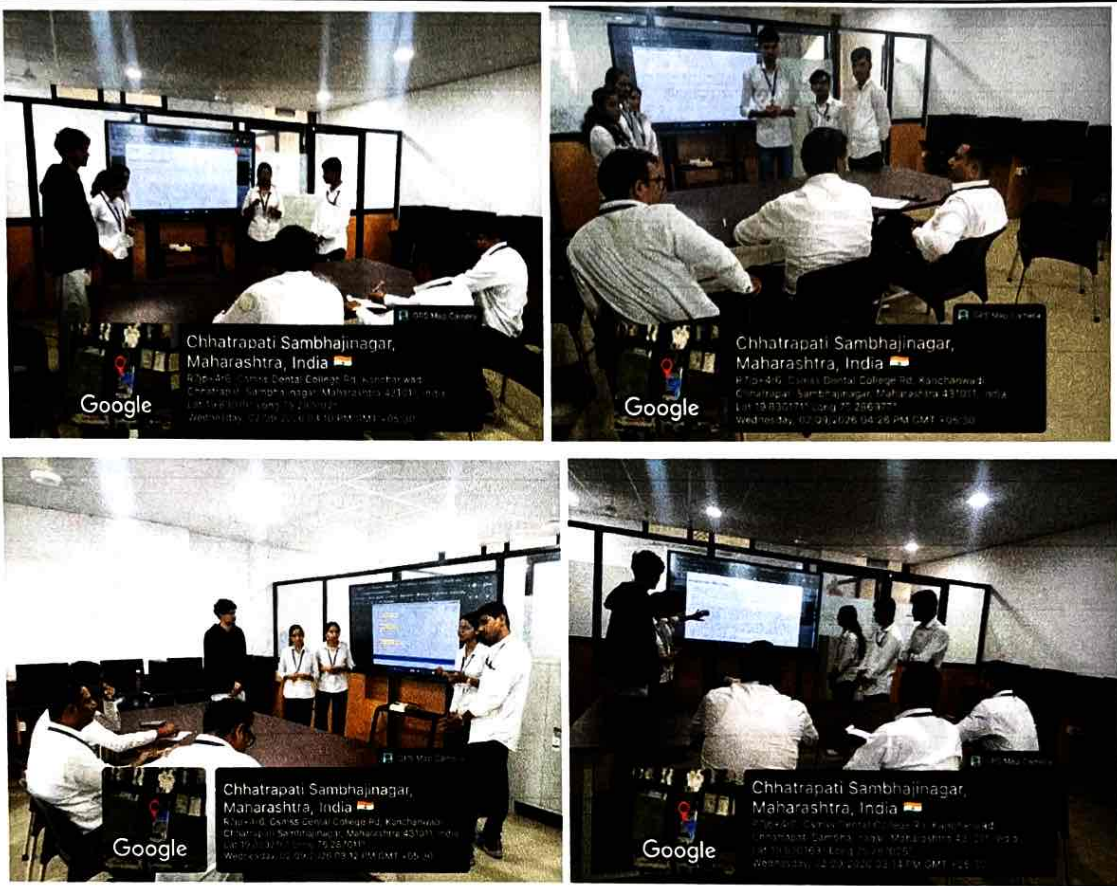
विद्येनेच मनुष्या आले प्रेरणाय ।

New Delhi, DTE (Govt. of Maharashtra) and affiliated to Dr. BATU, Lonere (Raigad). **DTE Code: 2533**

Department of EE (VLSI D&T)

Research & Development Cell (R&DC) and Institution Innovation Council Activity Report

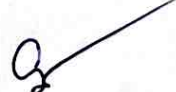
Name of the event:	DEPARTMENT LEVEL INTERNAL HACKATHON 2026					
Date:	2-9-2026					
Under the Initiative:	Department Level Internal Hackathon - Research & Development Cell (R&DC) and Institution Innovation Council Activity					
Team Details:	Registered :3	Participating: 3	Software Teams :02	Hardware Teams :01	Shortlisted: 03	Waitlisted: 00
Names and designations of Jury Members	Name: Dr.S.J. Honade Designation: Professor		Name: Prof. G.G. Patil Designation: Assistant Professor		Name: Prof.K.B. Dandge Designation: Assistant Professor	
Target Audience:	SY, TY,B.Tech Students					
Registration Link:	https://forms.gle/S4BUMnKWbnJiiW8E7					
Venue / Platform / Mode:	Lab - A203					
Dept. Coordinator:	Prof. T.A. Mohije Dept. EE (VLSI D&T)					
Objective:	Inculcate project development skills in students by working as per guidelines given in SIH to solve the industry and society problems.					
Overview	Smart India Hackathon (SIH) is a premier nationwide initiative designed to engage students in solving some of the most pressing challenges faced in everyday life. Launched to foster a culture of innovation and practical problem-solving, SIH provides a dynamic platform for students to develop and showcase their creative solutions to real-world problems. By encouraging participants to think critically and innovatively, the hackathon aims to bridge the gap between academic knowledge and practical application. Since its inception, SIH has garnered significant success in promoting out-of-the-box thinking among young minds, particularly engineering students from across India. Each edition has built on the previous one, refining its approach and expanding its impact. The hackathon not only offers students an opportunity to showcase their skills but also encourages collaboration with industry experts, government agencies, and other stakeholders.					
Participation	3 Groups participated in the evaluation process 1. Team Name: Debug SIH Problem Statement: 26077					

	2. Team Name: Tech Minds Problem Statement: 26050 3. Team Name: Innovatex Problem Statement: 26154
Key Outcomes:	Project problem statement finalization and decided with implementation methodology
Key observations, recommendations	1. Highlight the innovation/novelty and clearly differentiate the project from existing solutions. 2. Selection of microcontroller, sensors, communication protocols, software tools, and algorithms.
Conclusion:	Completed assessment with input to the student for project implementation.
Photo:	 Event Geotag - Photographs of Department Level Internal Hackathon on 2-9-2026.
Enclosure:	1. Copy of Official Event Circular .

Activity Report
Prepared by

 Prof. T.A. Mohije
 Dept. Coordinator


 Dr.S.J. Honade
 HOD


 Prof. P. D. Sathya
 SIH - SPOC


Approved By
 Dr. G. B. Dongre
 Principal



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INTERNAL HACKATHON 2026 EVALUATION (Dept. of EE (VLSI D&T)) Date: 2 / 9 / 2026

Sr. No	SIH Problem Statement Id	Team Name	Innovation / Novelty (10 Marks)	Feasibility & Technical Information (10 Marks)	Clarity & PPT Completeness (10 Marks)	Presentation Delivery (10 Marks)	Complexity and Future Scope (10 Marks)	Total (50 Marks)	Remark
1	26077	DEBUG SIH	08	08	09	708	08	41	
2	26050	Tech Minds	07	07	09	708	08	39	
3	26154	Innovatech	07	07	08	08	07	37	
4									
5									
6									
7									



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INTERNAL HACKATHON 2026 EVALUATION (Dept. of EE (VLSI D & T)) Date: 02/09/2026

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9									
10									
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Evaluator / Judge Name :

Prof. G. G. Patil

Prof. K. B. Dandge

Signature

Dept. Level SIH Coordinator

T. A. Monise

HOD

Institute SIH - SPOC

Principal