About the College

The Chhatrapati Shahu Maharaj Shikshan Sanstha's (CSMSS) Chhatrapati Shahu College of Engineering (DTE Code 2533) was established in the year 2013, aiming to develop the next generation of competent technocrats for industry and society. The institute is committed to ensuring the holistic development of students by imparting value and skill-based education, inculcation of skills and professional values, and providing a conducive system and environment for entrepreneurship, research, and innovation.

The Institute is located within the Municipal Corporation area of Chhatrapati Sambhajinagar (Aurangabad). It is located in the educational environment having various colleges such as Dental, Ayurved, Agriculture, Polytechnic, and Engineering, and is just 2 km from Chhatrapati Sambhajinagar Railway Station and 4 km from Central Bus Stand.

The college has eight Engineering Departments offering various B.Tech programs in ME, EE, CSE, Civil, AI & DS, Electronics & Computer Engineering, Electronics & Communication Engg. (ACT) and Electronics Engg. (VLSI Design & Technology (VDT)). The college runs six M.Tech programs in CSE, ETC, ME, Civil, ACT, and VDT. Special attention is given to interdisciplinary research. The institute has an excellent placement record with several top-ranking companies visiting the campus every year.

Organizing Committee

Chief Patron

Mr. Ranjeet P. Mulay
President CSMSS

Mr. Padmakar H. Mulay
Secretary, CSMSS

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Dr. Shrikant G. Deshmukh
Administrative Officer, CSMSS

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Principal,

CSMSS, Chh. Shahu College of Engineering

Convener & Coordinator

Dr. Shrikant J. Honade

HoD, Electronics Engg. (VLSI Design & Tech.)

Co-Coordinator

Dr. Jitesh R. Shinde,
Associate Professor, EE (VLSI D & T)

ATAL Academy

AICTE Training and Learning (ATAL) Academy is established with the vision "To empower faculty to achieve goals of Higher Education such as access, equity and quality". AICTE is committed to the development of quality technical education in the country by initiating various schemes launched by Govt. of India, Ministry of Human Resource Development. Training is required to increase the knowledge and skills of students to make them more employable to acquire global competencies.
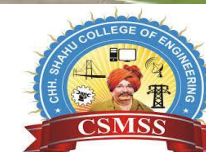
One Week AICTE Training & Learning (ATAL) Academy
Faculty Development Programme (Offline)

On

Semiconductor Design & Its Integration with Disruptive Technological Innovations

9th September to 14th September 2024

Organized By

DEPARTMENT OF ELECTRONICS ENGINEERING (VLSI DESIGN & TECHNOLOGY)

CSMSS
CHH. SHAHU COLLEGE OF ENGINEERING

Approved by AICTE, New Delhi, Affiliated to Affiliated to Dr. Babasaheb Ambedkar Technological University, Lonere
Kanchanwadi, Paithan Road, Chhatrapati Sambhajinagar (Aurangabad), MS, India – 431 002
http://www.csmssengg.org/

About the Department

Department of Electronics Engineering (VLSI Design and Technology) has a strong administration, infrastructure, and team of dedicated and experienced Faculty. Highly meritorious students are getting admitted to this program, not only from Maharashtra but all over India. In addition to core subjects related to the branch, VLSI core industry standard-specific and design-specific subjects are also included in the syllabus in line with the New Education Policy 2020. The department aims to develop VLSI-based industry-competent professionals of global standards capable of accepting challenges and able to solve societal problems at a larger level.

The Department offers two courses viz undergraduate course- B.Tech. Electronics Engineering (VLSI Design and Technology) and Post Graduate Course - M.Tech. Electronics and Communication (VLSI Design) affiliated with Dr. Babasaheb Ambedkar Technological University, Lonere, Raigad. The department caters to the ever-increasing needs of technical brilliance in all allied areas of electronics and VLSI Design and Technology.

ABOUT THE PROGRAMME

Semiconductor design is undergoing a rapid transformation, driven by the increasing demand for smaller, faster, and more energy-efficient chips. This six-day faculty development program (FDP) will equip engineering faculties, researchers, and professionals with the latest advancements in semiconductor design and its critical integration with disruptive technological innovations.

Participants gain hands-on experience using industry-standard EDA tools preferably FOSS. This FDP has been planned to bridge the gap between engineering education at academic & industry levels.

RESOURCE PERSONS

Experts from Industries like Intel, AMD, Spaarta Soft Solutionss, Pantech Elearning, JDMTech Semiconductors Pvt Ltd, etc., & experts from academia from the institutes of national or international importance like VNIT, NIT-W, NIELIT, VIT, etc.

ELIGIBILITY

- The training program is open to academicians / Ph.D. scholars / PG students / Industry professionals having an electronics & IT background.
- Min / Max limit: 30 / 50 participants

MODE & REGISTRATION

- Live classroom session in offline mode
- Registration is free and has to be done only through https://atalacademy.aicte-india.org/
- For more information, kindly visit: https://atalacademy.aicte-india.org/FAQs
- After successful registration, must fill the Google form: https://tinyurl.com/3nxj9jk9
- **Must read important instructions for participants, click link or scan:** https://tinyurl.com/mt25kzaj
- **For Registration Enquiries or any queries**: Dr. Jitesh R. Shinde, FDP Co-cordinator (Mb. 9325092503, jrshinde@csmssengg.org)

Important Dates & Registration Enquiries

- Apply on or before: 31-08-2024
- Intimation to selected candidates (email): 02-09-2024

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